

Notice of References Cited	Application/Control No. 10/528,332		Applicant(s)/Patent Under Reexamination OVCHINNIKOV, VALERY VASI	
	Examiner JUAN A. TORRES		Art Unit 2611	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-3,732,493 A	05-1973	Pelkey, William H.	324/166
*	B	US-5,014,315 A	05-1991	Polyak et al.	379/93.06
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Lattice, "In-System Programming Design Guidelines for ispJTAG Devices", February 2002
	V	Maxim, "DS2409 MicroLAN Coupler", February 7, 2003
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.